

GP 243

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Hronik, Stanley A.
 Assignee: Integrated Device Tech., Inc.
 Title: DOUBLE DATA RATE SYNCHRONOUS SRAM WITH 100% BUS UTILIZATION
 Serial No.: 09/347,106 Filed: July 2, 1999
 Examiner: Unknown Group Art Unit: 2751
 Docket No.: M-7086 US

GROUP 2700

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San Jose, California
 February 8, 2000

ASSISTANT COMMISSIONER FOR PATENTS
 Washington, D. C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Copies of these documents are enclosed.

Citation of these documents shall not be construed as:

1. an admission that the documents are necessarily prior art with respect to the instant invention;
2. a representation that a search has been made, other than as described above; or
3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on Feb. 8, 2000.

Michael Shenker 2-8-00
 Attorney for Applicant(s) Date of Signature

Respectfully submitted,

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